### ADHIYAMAAN COLLEGE OF ENGINEERING



(Autonomous)



### STRATEGIC PLAN OF ECE DEPARTMENT

### **VISION**

To develop well-disciplined and competent engineers who will excel in the field of Electronics and Communication Engineering.

### **MISSION**

- ♣ To develop qualified technical personnel with a strong knowledge on basic engineering principles.
- ♣ To disseminate Innovative technical skills by fostering excellence in engineering education.
- ♣ To promote exemplary professional conduct, to be utilised for the betterment of the society.

### Strategic development plan:

The strategic development plan is a way achieve the Institutional Vision, Mission and short term/long term goals. It is an organizational time destined activity that is used to set priorities keeping in view of the Vision, Mission, resources, ensuring that employees and other stakeholders are working toward common goals & outcomes/results. SWOC analysis is taken into consideration while developing the strategic plan.

# **Departmental SWOC Analysis**

# • Admission • Infrastructure • Well-Designed Curriculum • Recognized Research Department

Academic Results

Extra-Curricular

Co-Curricular &

Activities.

 Downfall of Admission in PG

Weaknesses

- Need to inculcate Research Projects
- Increase Entrepreneurship Opportunities
- Minimal Consultancy Projects

# Opportunities

- More job opportunities due to the Semiconductor revolution
- Emerging areas of R & D activities
- Opportunities in industries like consumer electronics manufacturing organization, Automotive, Telecommunication & IT industries, Health care equipment manufacturing, Internet technologies, and so on..

## Challenges

- Bring core industries for On-Campus Placements
- Aggressive
  Competition in
  Technological
  Advancement
- Regional and Online Competition

### Future Plans for the Department of Electronics and Communication Engineering:

- NBA Accreditation
- Upgradation of Laboratories with modern Equipments
- To establish better tie-ups with reputed industries by signing MoUs to conduct hands-on training for the students.
- To enhance the participation in an intercollege design competitions, workshops, National and International conferences and other events.
- To Organize Industrial Visits, encourage Internships for all students, Technical Symposiums, Extra-curricular activities.
- To increase Journal publications of faculties in Scopus/ Web of Science.
- To Establish Centre of Excellence in Semiconductor Chip Design
- To Establish Embedded systems and IoT Laboratory
- Fill curriculum gap between industrial and Academia.
- To Encourage the students to do more industrial projects.
- To motivate students to participate in NSS activities.
- Concentrate more on PG Admission
- Establish separate project laboratory.
- Awareness and preparation for Higher studies
- To conduct more events in association with professional societies like ISTE, IETE, IEI,etc.,
- Planning to conduct Mini Projects and project expo for students

### The implementation of above plans will result in:

- Excel in Semiconductor and automotive embedded applications through innovative ideas
- More research project ideas
- Students may register for more competitive exams
- Increase in the number of faculty members pursuing Ph.D.
- Students may participate in Project expo, paper presentations, hackathons etc.,
- Increase in journal paper publications by the students

### **Implemented Plans:**

- Signed MoUs with Industries
- Workshops, Hands-on training, Seminars were organized for the student benefits
- Value-added courses were conducted with industry experts
- Seminars related to Intellectual Property Rights were conducted to create awareness among students and faculty members