

**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**  
(Autonomous)  
**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**

**Minutes of the Twenty-sixth Meeting**

Minutes of the 26<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering  
22.07.2022 held on at 11.00 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, Chairman
2. Dr. T Menakadevi, Internal Member
3. Dr.R.Poovendran, Internal Member
4. Mr. M. AshokKumar, Internal Member
5. Dr.T.S.UdhayaSuriya , Interdisciplinary Member, HoD/BME
6. Dr.B.Perumal, Interdisciplinary Member, Dept of EEE
7. Dr.K.Senthil, Interdisciplinary Member, Dept of EEE
8. Prof.Dr.A.Sivanantha Raja, University Nominee
9. Prof.Dr.K.Mariammal, Academic Council Nominee
10. Prof.Dr.M.Dhinakaran, Academic Council Nominee
11. Smt.SudhaM, Industry Representative
12. Mr. S. Munirajsankar, Alumnus Member

- 26.1 To consider the minutes of the 25<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 18.09.2021.

**RESOLVED** that the minutes of the 25<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 18.09.2021. have been considered.

- 26.2 To consider the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019– 2020.

**RESOLVED** that the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019– 2020 be approved (Annexure I)

- 26.3 To consider the EEC category elective course entitled “Professional Readiness for Innovation, Employability and Entrepreneurship” approved from the Centre for Academic Courses, Chennai for 7<sup>th</sup> semester B.E Electronics and Communication Engineering programme under the regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the EEC category elective course entitled “Professional Readiness for Innovation, Employability and Entrepreneurship” approved from the Centre for Academic Courses, Chennai for 7<sup>th</sup> semester B.E Electronics and Communication Engineering

programme under the regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure- II).

- 26.4 To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

**RESOLVED** that the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved(Annexure III)

- 26.5 To consider the Syllabi of “Microprocessors and Microcontrollers” and “Microprocessors and Microcontrollers Laboratory” for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

**RESOLVED** that the Syllabi of “Microprocessors and Microcontrollers” and “Microprocessors and Microcontrollers Laboratory” for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure IV)

- 26.6 To consider the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021 – 2022.

**RESOLVED** the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021 – 2022 be approved. (Annexure V)

- 26.7 To consider the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022.

**RESOLVED** that the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022 be approved (Annexure VI)

- 26.8 To consider the Syllabus of “Communication Engineering” for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022.

**RESOLVED** the Syllabus of “Communication Engineering” for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2021 – 2022 be approved (Annexure VII)

26.9 To consider the curriculum of B.E Electronics and Communication Engineering under the Regulations 2022 to be followed as per choice based credit systems for the batch of the students to be admitted during 2022-2023. (Under Processing)

26.10 To consider the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021-2022.

**RESOLVED** the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2021-2022 be approved. (Annexure IX)

26.11 To consider the curriculum of M.E Communication Systems under the Regulations 2022 to be followed as per choice-based credit systems for the batch of the students to be admitted during 2022-2023.

**RESOLVED** consider the curriculum of M.E Communication Systems under the Regulations 2022 to be followed as per choice based credit systems for the batch of the students to be admitted during 2022-2023 be approved. (Annexure X)

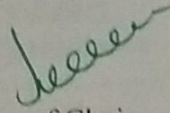
26.12 To consider the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students to be admitted in 2022-2023.

**RESOLVED** the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2022 (CBCS) for the batch of students to be admitted in 2022-2023 be approved. (Annexure XI)

26.14 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2022-2023.

**RESOLVED** consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2022-2023 be approved. (Annexure XIII)

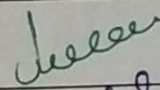
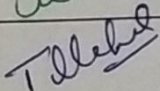
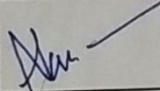
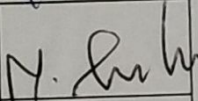
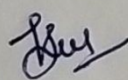
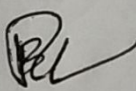
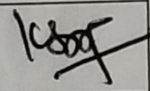
Date: 22.07.2022

  
Signature of Chairman –BoS

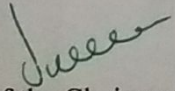
**Chairman, Board of Studies**  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhityamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twenty Sixth Board of Studies Meeting held on 22.07.2022

| Sl.No. | Name & Address of the Members   | Category                       | Signature   |
|--------|---|--------------------------------|---|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.  | BoS-Chairman                   |    |
| 2      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 3      | Dr.R.Poovendran.,P.h.D.,<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 4      | Mr. M. Ashok Kumar, M.E, Assistant professor, Dept. of<br>ECE, ACE, Hosur.  | Internal Member                |    |
| 5      | Dr.T.S.UdhayaSuriya<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |    |
| 6      | Prof.Dr.B.Perumal, Ph.D.,Asso.Professor& Head/Dept. of<br>EEE<br>Adhiyamaan College of Engineering, Hosur.  | Interdisciplinary<br>Member    |   |
| 7      | Prof.Dr.K.Senthil, Ph.D.,Asso.Professor/Dept. of EEE<br>Adhiyamaan College of Engineering, Hosur.   | Interdisciplinary<br>Member    |  |
| 8      | Prof.Dr.ASivanantha Raja.,Ph.D Professor & Head ,Dept.<br>of ECE,<br>AlagappaChettiar Government College of Engineering<br>and Technology,Karaikudi             | University<br>Nominee          | Online mode   |
| 9      | Prof.Dr. K.Mariammal.,Ph.D., Assistant Professor (Sr<br>Gr), Department of Electronics Engineering, Madras<br>Institute of Technology Anna University, Chennai. | Academic<br>Council<br>Nominee | Online mode   |
| 10     | Prof.Dr. M.Dhinakaran.,Ph.D., Associate Professor,<br>Dept. of ECE,Government College of Engineering,<br>Salem  | Academic<br>Council<br>Nominee | Online mode   |
| 11     | Smt. Sudha M Larsen and Toubro (L & T)<br>Defence, Bangalore ,  | Industry<br>Representative     | Online mode   |
| 12     | Mr. S. MunirajSankar<br>Senior Specialist , HCL Technologies Ltd ,Chennai   | Alumnus<br>Member              | Online mode   |

Date: 22.07.2022

  
Signature of the Chairman-BoS

**Chairman, Board of Studies**  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**  
(Autonomous)  
**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**

**Minutes of the Twenty-Fifth Meeting**

Minutes of the 25<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 18.09.2021 at 09.30 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, Chairman
2. Dr. T Menakadevi, Internal Member
3. Dr.R.Poovendran, Internal Member
4. Dr.N.Nagaraju, Internal Member
5. Dr.T.S.UdhayaSuriya , Interdisciplinary Member, HoD/BME
6. Dr.B.Perumal, Interdisciplinary Member, Dept of EEE
7. Dr.K.Senthil, Interdisciplinary Member, Dept of EEE
8. Prof.Dr.A.Sivanantha Raja, University Nominee
9. Prof.Dr.K.Mariammal, Academic Council Nominee
10. Prof.Dr.M.Dhinakaran, Academic Council Nominee
11. Smt.Sudha M, Industry Representative
12. Mr. S. Munirajsankar, Alumnus Member

- 25.1** To consider the minutes of the 24<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 27.03.2021.

**RESOLVED** that the minutes of the 24<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 27.03.2021 have been considered.

- 25.2** To consider the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018– 2019.

**RESOLVED** that the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018– 2019 be approved (Annexure I)

- 25.3** To consider the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** that the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure II)

25.4 To consider the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure III)

25.5 To consider the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021 – 2022.

**RESOLVED** the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021 – 2022 be approved. (Annexure IV)

25.6 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021 be approved (Annexure V)

25.7 To consider the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022.

**RESOLVED** that the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022 be approved (Annexure VI)


25.8 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure VII)

25.9 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022.

**RESOLVED** that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022 be approved. (Annexure VIII)

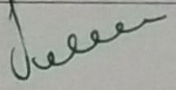
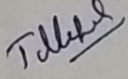
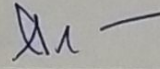
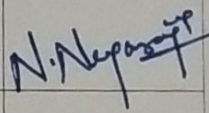
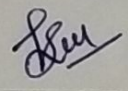
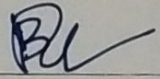
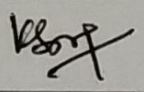
Date: 07.10.2020

  
Signature of Chairman –BoS

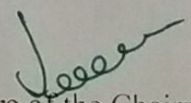
Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering  
Adityan College of Engineering (Autonomous)  
Hosur-535199  
Krishnagiri (R.D.) Taluk, N.Du.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twenty Fifth Board of Studies Meeting held on 18.09.2021

| Sl.No. | Name & Address of the Members  | Category                       | Signature   |
|--------|--|--------------------------------|---|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.   | BoS-Chairman                   |    |
| 2      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 3      | Dr.R.Poovendran.,M.E.,(VLSI Design )<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 4      | Prof.Dr. N.Nagaraju., Ph.D.,<br>Asst. Professor, Dept.of ECE, ACE,Hosur  | Internal Member                |    |
| 5      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |    |
| 6      | Prof.Dr.B.Perumal, Ph.D.,Asso.Professor /Dept. of EEE<br>Adhiyamaan College of Engineering, Hosur.   | Interdisciplinary<br>Member    |   |
| 7      | Prof.Dr.K.Senthil, Ph.D.,Asso.Professor /Dept. of EEE<br>Adhiyamaan College of Engineering, Hosur.   | Interdisciplinary<br>Member    |  |
| 8      | Prof.Dr.A Sivanantha Raja.,Ph.D Professor & Head<br>,Dept. of ECE,<br>Alagappa Chettiar Government College of Engineering<br>and Technology,Karaikudi            | University<br>Nominee          | Online mode   |
| 9      | Prof.Dr. K.Mariammal., Ph.D., Assistant Professor (Sr<br>Gr), Department of Electronics Engineering, Madras<br>Institute of Technology Anna University, Chennai. | Academic<br>Council<br>Nominee | Online mode   |
| 10     | Prof.Dr. M.Dhinakaran.,Ph.D., Assistant Professor,<br>Dept. of ECE, Government College of Engineering,<br>Salem  | Academic<br>Council<br>Nominee | Online mode   |
| 11     | Smt. Sudha M Larsen and Toubro (L & T)<br>Defence, Bangalore ,   | Industry<br>Representative     | Online mode   |
| 12     | Mr. S. Muniraj Sankar<br>Senior Specialist , HCL Technologies Ltd ,Chennai   | Alumnus<br>Member              | Online mode   |

Date: 18.09.2021

  
Signature of the Chairman-BoS

**Chairman, Board of Studies**  
**Faculty of Electronics and Communication Engineering (UG & FC)**  
**Adhiyamaan College of Engineering (Autonomous)**  
**Hosur - 635 109**  
**Krishnagiri (Dt), Tamil Nadu.**

**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**  
(Autonomous)  
**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**

**Minutes of the Twenty-Fourth Meeting**

Minutes of the 24<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 27.03.2021 at 11.00 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, Chairman
2. Dr. T Menakadevi, Internal Member
3. Dr. R. Poovendran, Internal Member
4. Dr. N. Nagaraju, Internal Member
5. Prof. S. Vijay Murugan, Internal Member
6. Dr. T. S. Udhaya Suriya, Interdisciplinary Member, HoD/BME
7. Dr. B. Perumal, Interdisciplinary Member, Dept of EEE
8. Prof. Dr. A. Sivanantha Raja, University Nominee
9. Prof. Dr. K. Mariammal, Academic Council Nominee
10. Prof. Dr. M. Dhinakaran, Academic Council Nominee
11. Smt. Sudha M, Industry Representative
12. Mr. S. Munirajsankar, Alumnus Member

- 24.1 To consider the minutes of the 23<sup>rd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 07.10.2020.

**RESOLVED** that the minutes of the 23<sup>rd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 07.10.2020. have been considered.

- 24.2 To consider the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018 – 2019..

**RESOLVED** that the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018 – 2019 be approved (Annexure I)

- 24.3 To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.



**RESOLVED** that the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure II)

- 24.4 To consider the Syllabi of “Microprocessors and Microcontrollers” and “Microprocessors and Microcontrollers Laboratory” for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** that the Syllabi of “Microprocessors and Microcontrollers” and “Microprocessors and Microcontrollers Laboratory” for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure III)

- 24.5 To consider the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure IV)

- 24.6 To consider the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

**RESOLVED** that consider the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure V)

- 24.7 To consider the Syllabus of “Communication Engineering” for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021.

**RESOLVED** that the Syllabus of “Communication Engineering” for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2020 – 2021 be approved (Annexure VI)

- 24.8 To consider the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021 be approved (Annexure VII)

- 24.9 To consider the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022.

**RESOLVED** that consider the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2021-2022 be approved (Annexure VIII)

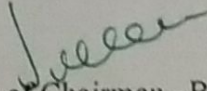
- 24.10 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure IX)

- 24.11 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022.

**RESOLVED** that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2021-2022 be approved. (Annexure X)

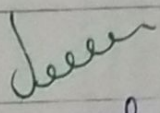
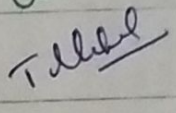

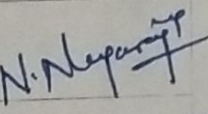
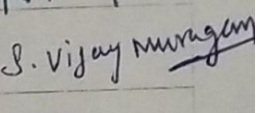
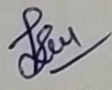
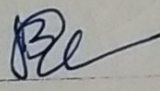
Date: 27.03.2021

  
Signature of Chairman –BoS

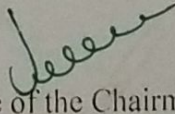
Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhimaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twenty Fourth Board of Studies Meeting held on 27.03.2021

| Sl.No. | Name & Address of the Members  | Category                       | Signature   |
|--------|--|--------------------------------|---|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.   | BoS-Chairman                   |    |
| 2      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 3      | Dr.R.Poovendran.,M.E.,(VLSI Design )<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 4      | Prof.Dr. N.Nagaraju., Ph.D.,<br>Asst. Professor, Dept.of ECE, ACE,Hosur  | Internal Member                |    |
| 5      | Prof.S.Vijay Murugan ,M.E.,<br>Asso.Professor , Dept. of ECE , ACE, Hosur.   | Internal Member                |    |
| 6      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |    |
| 7      | Prof.Dr.B.Perumal, Ph.D.,Asso.Professor /Dept. of EEE<br>Adhiyamaan College of Engineering, Hosur.   | Interdisciplinary<br>Member    |  |
| 8      | Prof.Dr.A Sivanantha Raja.,Ph.D Professor & Head<br>,Dept. of ECE,<br>Alagappa Chettiar Government College of Engineering<br>and Technology,Karaikudi            | University<br>Nominee          | Online mode   |
| 9      | Prof.Dr. K.Mariammal., Ph.D., Assistant Professor (Sr<br>Gr), Department of Electronics Engineering, Madras<br>Institute of Technology Anna University, Chennai. | Academic<br>Council<br>Nominee | Online mode   |
| 10     | Prof.Dr. M.Dhinakaran.,Ph.D., Assistant Professor,<br>Dept. of ECE, Government College of Engineering,<br>Salem  | Academic<br>Council<br>Nominee | Online mode   |
| 11     | Smt. Sudha M Larsen and Toubro (L & T)<br>Defence, Bangalore ,   | Industry<br>Representative     | Online mode   |
| 12     | Mr. S. Muniraj Sankar<br>Senior Specialist , HCL Technologies Ltd ,Chennai   | Alumnus<br>Member              | Online mode   |

Date: 27.03.2021

  
Signature of the Chairman-BoS

**Chairman, Board of Studies**  
**Faculty of Electronics and Communication Engineering (UECE)**  
**Adhiyamaan College of Engineering (Autonomous)**  
**Hosur - 635 109**  
**Krishnagiri (Dt), Tamil Nadu.**

**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**  
(Autonomous)  
**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**

**Minutes of the Twenty-Fourth Meeting**

Minutes of the 23<sup>rd</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 07.10.2020 at 11.00 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, Chairman
2. Dr. T Menakadevi, Internal Member
3. Prof.M.Sivakumar, Internal Member
4. Prof.Dr.R.Poovendran, Internal Member
5. Prof.Dr.N.Nagaraju, Internal Member
6. Prof.S.Vijay Murugan, Internal Member
7. Dr.T.S.Udhaya Suriya , Interdisciplinary Member, HoD/BME
8. Prof.Dr.A Sivanantha Raja, University Nominee
9. Prof.Dr. K.Mariammal, Academic Council Nominee
10. Prof.Dr. M.Dhinakaran, Academic Council Nominee
11. Smt. Sudha M, Industry Representative
12. Mr. S. Muniraj Sankar, Alumnus Member

- 23.1 To consider the minutes of the 22<sup>nd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 02.11.2019.

**RESOLVED** that the minutes of the minutes of the 22<sup>nd</sup> Board of Studies meeting in Electronics and Communication Engineering held on 02.11.2019 have been considered.

- 23.2 To consider the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017- 2018.

**RESOLVED** that the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017- 2018 be approved (Annexure I)

- 23.3 To consider the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 - 2018.

**RESOLVED** that the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 - 2018 be approved (Annexure II)

23.4 To consider the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 – 2019.

**RESOLVED** that the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 – 2019 be approved (Annexure III)

23.5 To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 – 2019.

**RESOLVED** that the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 – 2019 be approved (Annexure IV)

23.6 To consider the Syllabi of “Microprocessors and Microcontrollers with Applications” and “Microprocessors and Microcontrollers Laboratory” for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 –2019.

**RESOLVED** that the Syllabi of “Microprocessors and Microcontrollers with Applications” and “Microprocessors and Microcontrollers Laboratory” for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 –2019 be approved (Annexure V)

23.7 To consider the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure VI)

23.8 To consider the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure VII)

23.9 To consider the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** that consider the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information

Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved (Annexure VIII)

- 23.10 To consider the Syllabus of “Communication Engineering” for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** the Syllabus of “Communication Engineering” for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved. (Annexure IX)

- 23.11 To consider the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020 – 2021.

**RESOLVED** the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020 – 2021 be approved. (Annexure X)

- 23.12 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XI)

- 23.13 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XII)

- 23.14 To consider the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XIII)

- 23.15 To consider the Syllabi of 3<sup>rd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure XIV)

23.16 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021.

**RESOLVED** that the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2020-2021 be approved (Annexure XV)

23.17 To consider the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020-2021.

**RESOLVED** that the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2020-2021 be approved (Annexure XVI)

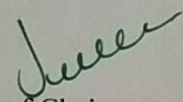
23.18 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure XVII)

23.19 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2020-2021.

**RESOLVED** that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2020-2021 be approved. (Annexure XVIII)

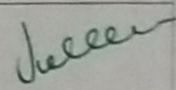
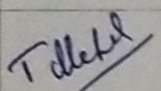

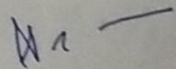
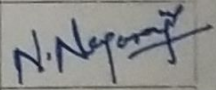
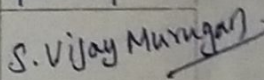
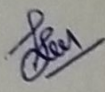
Date: 07.10.2020

  
Signature of Chairman –BoS

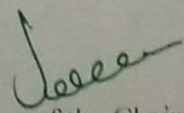
**Chairman, Board of Studies**  
**Faculty of Electronics and Communication Engineering (UG & P.C)**  
**Adiyamaan College of Engineering (Autonomous)**  
**Madur - 636 100**  
**Kotahnagiri (D), Tamil Nadu.**

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twenty third Board of Studies Meeting held on 07.10.2020

| Sl.No. | Name & Address of the Members  | Category                       | Signature   |
|--------|--|--------------------------------|---|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.   | BoS-Chairman                   |    |
| 2      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 3      | Mr.M.Sivakumar,M.E(Communication Systems),<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 4      | Dr.R.Poovendran.,M.E.,(VLSI Design )<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 5      | Prof.Dr. N.Nagaraju., Ph.D.,<br>Asst. Professor, Dept.of ECE, ACE,Hosur  | Internal Member                |    |
| 6      | Prof.S.Vijay Murugan ,M.E.,<br>Asso.Professor , Dept. of ECE , ACE, Hosur.   | Internal Member                |   |
| 7      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |  |
| 8      | Prof.Dr.A Sivanantha Raja.,Ph.D Professor & Head<br>,Dept. of ECE,<br>Alagappa Chettiar Government College of Engineering<br>and Technology,Karaikudi            | University<br>Nominee          | Online mode   |
| 9      | Prof.Dr. K.Mariammal., Ph.D., Assistant Professor (Sr<br>Gr), Department of Electronics Engineering, Madras<br>Institute of Technology Anna University, Chennai. | Academic<br>Council<br>Nominee | Online mode   |
| 10     | Prof.Dr. M.Dhinakaran.,Ph.D., Assistant Professor,<br>Dept. of ECE, Government College of Engineering,<br>Salem  | Academic<br>Council<br>Nominee | Online mode   |
| 11     | Smt. Sudha M Larsen and Toubro (L & T)<br>Defence, Bangalore ,   | Industry<br>Representative     | Online mode   |
| 12     | Mr. S. Muniraj Sankar<br>Senior Specialist , HCL Technologies Ltd ,Chennai   | Alumnus<br>Member              | Online mode   |

Date: 07.10.2020

  
 Signature of the Chairman-BoS  
**Chairman, Board of Studies**  
 Faculty of Electronics and Communication Engineering (EC & EC)  
 Adhiyamaan College of Engineering (Autonomous)  
 Hosur - 635 109  
 Krishnagiri (Dt), Tamil Nadu.



ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109  
(Autonomous)  
BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Twenty-second Meeting

Minutes of the 22<sup>nd</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 02.11.2019 at 10.30 A.M.

MEMBERS PRESENT

1. Dr. S. Sumathi, Chairman
2. Dr. K.A.Dattatreya, Internal Member
3. Dr. T Menakadevi, Internal Member
4. Prof.M.Sivakumar, Internal Member
5. Prof.R.Poovendran, Internal Member
6. Dr.T.S.Udhaya Suriya , Interdisciplinary Member, HoD/BME
7. Prof.Dr.Narayanappa, Interdisciplinary Member, HoD/EEE
8. Prof.Dr.N.Ramadass, Special Invitee
9. Prof.Dr.D.Seshachalam, Academic Council Nominee
10. Prof.Dr.G.Kavitha, Academic Council Nominee
11. Smt.N.Dhivya, Industry Representative
12. Shri.R.Chandrashekar, Alumnus Member

- 22.1 To consider the minutes of the 21<sup>st</sup> Board of Studies meeting in Electronics and Communication Engineering held on 27.04.2019.

**RESOLVED** that the minutes of the 21<sup>st</sup> Board of Studies meeting in Electronics and Communication Engineering held on 27.04.2019 have been considered.

- 22.2 To consider the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017 be approved (Annexure I)

- 22.3 To consider the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure II)

22.4 To consider the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 – 2019.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018 – 2019 be approved (Annexure III)

22.5 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure IV)

22.6 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure V)

22.7 To consider the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Electron Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering) programme) under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020.

**RESOLVED** the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Electron Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering) programme) under the Regulations 2018 (CBCS) for the batch of students admitted in 2019 – 2020 be approved. (Annexure VI)

22.8 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure VII)

22.9 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020.

**RESOLVED** that the Syllabi of 2<sup>nd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2019-2020 be approved (Annexure VIII)

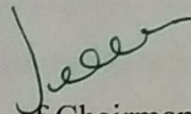
22.10 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure IX)

22.11 To consider the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020.

**RESOLVED** that the list of Examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020 be approved. (Annexure X)

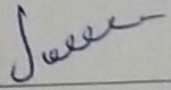
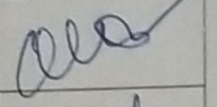
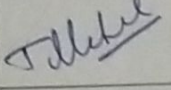

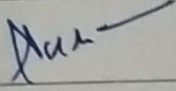
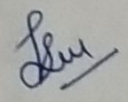
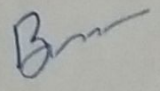
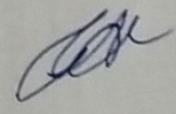

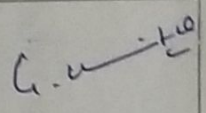
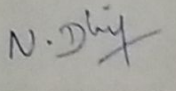

Date: 02.11.2019

  
Signature of Chairman –BoS

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhiyamaan College of Engineering (Autonomous)  
Hoour - 635 105  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twenty second Board of Studies Meeting held on 02.11.2019

| Sl.No. | Name & Address of the Members  | Category                       | Signature   |
|--------|--|--------------------------------|---|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.   | BoS-Chairman                   |    |
| 2      | Dr. K.A.Dattatreya, M.E (Electronics), MBA, Ph.D<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 3      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 4      | Mr.M.Sivakumar,M.E(Communication Systems),<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 5      | Mr.R.Poovendran.,M.E.,(VLSI Design )<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 6      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |    |
| 7      | Dr.Narayanappa, M.E., Ph.D.,<br>Professor & Head,<br>Department of EEE, ACE, Hosur.  | Interdisciplinary<br>Member    |   |
| 8      | Prof.Dr.N.Ramadass, M.E., Ph.D.,(Communication<br>Systems)<br>Professor/Department of ECE,<br>CEG Campus,Anna University, Chennai. | Special Invitee                |  |
| 9      | Prof.Dr. D. Seshachalam(Embedded systems)<br>Prof & Head/Dept. of ECE<br>BMS college of Engineering, Bangalore.                    | Academic<br>Council<br>Nominee |  |
| 10     | Prof.Dr.G.Kavithaa(Communication systems)<br>Assistant Professor/ Dept. of ECE,<br>Government College of Engineering, Salem.       | Academic<br>Council<br>Nominee |  |
| 11     | Smt.N.Dhivya,( Electronics)<br>Technical Lead,HCL technologies,<br>Jigani, Bangalore.  | Industry<br>Representative     |  |
| 12     | Shri..R.Chandrashekar,<br>HR,QAD Department<br>TVS- Hosur  | Alumnus<br>Member              |  |

Date: 02.11.2019

  
 Signature of the Chairman-BoS  
 Chairman, Board of Studies  
 Faculty of Electronics and Communication Engineering (UG & PG)  
 Adhiyamaan College of Engineering (Autonomous)  
 Hosur - 635 109  
 Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**

**(Autonomous)**

**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**

**Minutes of the Twenty-first Meeting**

Minutes of the 21<sup>st</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 27.04.2019 at 10.00 A.M.

**MEMBERS PRESENT**

1. Dr.S.Sumathi, Chairman
2. Dr.K.A.Dattatreya, Internal Member
3. Dr.T Menakadevi, Internal Member
4. Prof.M.Sivakumar, Internal Member
5. Prof.R.Poovendran, Internal Member
6. Dr.T.S.Udhaya Suriya , Interdisciplinary Member, HoD/BME
7. Prof.Dr.S.Jayakumar, Interdisciplinary Member, Asst.Prof/CHEM. ENGG.
8. Prof.Dr.N.Ramadass, University Nominee
9. Prof.Dr.G.Kavitha, Academic Council Nominee
10. Smt.N.Dhivya, Industry Representative
11. Shri.R.Chandrashekar, Alumnus Member

- 21.1 To consider the minutes of the 20<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 27.10.2018.

**RESOLVED** that the minutes of the 20<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 27.10.2018 have been considered.

- 21.2 To consider the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017 be approved (Annexure I)

- 21.3 To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure II)

- 21.4 To consider the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5<sup>th</sup> Semester B.E.(Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5<sup>th</sup> Semester B.E. (Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure III)

- 21.5 To consider the Syllabi of "Communication Engineering", "Embedded System Design", and "Advanced Microprocessor Laboratory" for 5<sup>th</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of "Communication Engineering", "Embedded System Design", and "Advanced Microprocessor Laboratory" for 5<sup>th</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure IV)

- 21.6 To consider the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018 – 2019.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018 – 2019 be approved (Annexure V)

- 21.7 To consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2018 – 2019.

**RESOLVED** consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering/Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2018 – 2019 be approved. (Annexure VI)

- 21.8 To consider the Syllabus of "Communication Engineering" for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2018 – 2019.

**RESOLVED** consider the Syllabus of "Communication Engineering" for 3<sup>rd</sup> Semester B.Tech. (Information Technology) programme under the Regulation 2018 (CBCS) for the batch of students admitted in 2018 – 2019 be approved. (Annexure VII)

21.9 To consider the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019.

**RESOLVED** the Syllabi of 3<sup>rd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure VIII)

21.10 To consider the Syllabi of 3<sup>rd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure IX)

21.11 To consider the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2019-2020.

**RESOLVED** the Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students to be admitted in 2019-2020 be approved. (Annexure X)

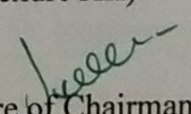
21.12 To consider the Syllabi of 1<sup>st</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students to be admitted in 2019-2020 .

**RESOLVED** that the Syllabi of 1<sup>st</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students to be admitted in 2019-2020 be approved. (Annexure XI)

21.13 To consider the list of examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020.

**RESOLVED** that the list of examiners for the conduction of examinations (Practical & Theory) for the academic year 2019-2020 be approved (Annexure XII)

Date: 27.04.2019

  
Signature of Chairman –BoS

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UO & TC)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twenty-first Board of Studies Meeting held on 27.04.2019

| Sl.No. | Name & Address of the Members   | Category                       | Signature                   |
|--------|---|--------------------------------|-----------------------------|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.  | BoS-Chairman                   | <i>Jeevan</i>               |
| 2      | Dr. K.A.Dattatreya, M.E (Electronics), MBA, Ph.D<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                | <i>[Signature]</i>          |
| 3      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                | <i>T. Menakadevi</i>        |
| 4      | Mr.M.Sivakumar,M.E(Communication Systems),<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                | <i>[Signature]</i>          |
| 5      | Mr.R.Poovendran.,M.E.,(VLSI Design )<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                | <i>[Signature]</i>          |
| 6      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.  | Interdisciplinary<br>Member    | <i>[Signature]</i>          |
| 7      | Dr.S.Jayakumar, M.E., Ph.D., (Applied Electronics)<br>Assistant Professor/Dept. of Chemical Engineering,<br>Adhiyamaan College of Engineering, Hosur. | Interdisciplinary<br>Member    | <i>[Signature]</i>          |
| 8      | Prof.Dr.N.Ramadass, M.E., Ph.D.,(Communication Systems)<br>Associate Professor/Department of ECE,<br>CEG Campus,Anna University, Chennai.             | University<br>Nominee          | <i>[Signature]</i>          |
| 9      | Prof.Dr. D. Seshachalam(Embedded systems)<br>Prof & Head/Dept. of ECE<br>BMS college of Engineering, Bangalore.                                       | Academic<br>Council<br>Nominee | <i>AB</i>                   |
| 10     | Prof.Dr.G.Kavitha(Communication systems)<br>Assistant Professor/ Dept. of ECE,<br>Government College of Engineering, Salem.                           | Academic<br>Council<br>Nominee | <i>[Signature]</i>          |
| 11     | Smt.N.Dhivya,( Electronics)<br>Technical Lead,HCL technologies,<br>Jigani, Bangalore.   | Industry<br>Representative     | <i>N. Dhivya</i><br>27/4/19 |
| 12     | Shri..R.Chandrashekar,<br>HR,QAD Department<br>TVS- Hosur   | Alumnus<br>Member              | <i>[Signature]</i>          |

Date: 27.04.2019

*Jeevan*  
Signature of the Chairman-BoS

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UO & FC)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.



**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**  
(Autonomous)  
**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**

**Minutes of the Twentieth Meeting**

Minutes of the 20<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 27.10.2018 at 10.00 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, Chairman
2. Dr. K.A.Dattatreya, Internal Member
3. Dr. T Menakadevi, Internal Member
4. Prof.M.Sivakumar, Internal Member
5. Prof.R.Poovendran, Internal Member
6. Dr.T.S.Udhaya Suriya , Interdisciplinary Member, HoD/BME
7. Prof.Dr.S.Jayakumar, Interdisciplinary Member, Asst.Prof/B.Tech CHEM
8. Prof.Dr.N.Ramadass, University Nominee
9. Prof.Dr. D. Seshachalam, Academic Council Nominee
10. Prof.Dr.G.Kavitha, Academic Council Nominee
11. Smt.N.Dhivya, Industry Representative
12. Shri..R.Chandrashekar, Alumnus Member

- 20.1** To consider the minutes of the 19<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 21.04.2018.

**RESOLVED** that the minutes of the 19<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 21.04.2018 have been considered.

- 20.2** To consider the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 – 2016.

**RESOLVED** that the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 – 2016 be approved (Annexure I)

- 20.3 To consider the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017 be approved (Annexure II)

- 20.4 To consider the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure III)

- 20.5 To consider the Syllabi of “Microprocessors and Microcontrollers” and “Signals and Systems” for 4<sup>th</sup> Semester B.Tech (Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of “Microprocessors and Microcontrollers” and “Signals and Systems” for 4<sup>th</sup> Semester B.Tech (Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure IV)

- 20.6 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (Communication Systems) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** that To consider the Syllabi of 4<sup>th</sup> Semester M.E. (Communication System) programme under the Regulations 2015(CBCS) for the batch of students admitted 2017-2018 be approved (Annexure V)

- 20.7 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018 be approved (Annexure VI)

- 20.8 To consider the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering) programme) under the Regulations 2018(CBCS) for the batch of students admitted 2018 – 2019.

**RESOLVED** that the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering) programme) under the Regulations 2018(CBCS) for the batch of students admitted in 2018 – 2019 be approved (Annexure VII)

20.9 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019.

**RESOLVED** that the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 (CBCS) for the batch of students admitted in 2018-2019 be approved (Annexure VIII)

20.10 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019.

**RESOLVED** the Syllabi of 2<sup>nd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018(CBCS) for the batch of students admitted in 2018-2019 be approved. (Annexure IX)

20.11 To consider the resolution pertaining to regulation -2018(CBCS).

- a) IA (Internal assessment) & EA (External Assessment) marks ratio.
- b) Continuous assessment (IA) Marks Allocation.
- c) End Assessment Question Paper Pattern.
- d) One Credit Courses over and above the total credits to be earned.

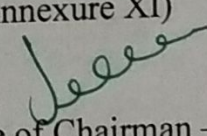
**RESOLVED** that the resolution pertaining to regulation -2018(CBCS).

- a) IA (Internal assessment) & EA (External Assessment) marks ratio.
  - b) Continuous assessment (IA) Marks Allocation.
  - c) End Assessment Question Paper Pattern.
  - d) One Credit Courses over and above the total credits to be earned.
- be approved. (Annexure X)

20.12 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** that the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering. be approved (Annexure XI)

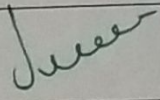
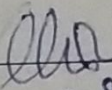
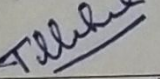

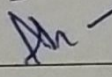
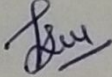
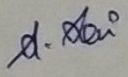
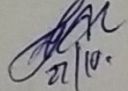
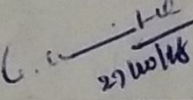
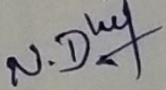
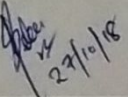
Date: 27.10.2018

  
Signature of Chairman –BoS

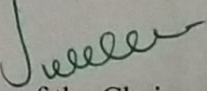
Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (Autonomous)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Twentieth Board of Studies Meeting held on 27.10.2018

| Sl.No. | Name & Address of the Members   | Category                       | Signature   |
|--------|---|--------------------------------|---|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.  | BoS-Chairman                   |    |
| 2      | Dr. K.A.Dattatreya, M.E (Electronics), MBA, Ph.D<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 3      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 4      | Mr.M.Sivakumar,M.E(Communication Systems).,<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |    |
| 5      | Mr.R.Poovendran.,M.E.,(VLSI Design )<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                |    |
| 6      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.  | Interdisciplinary<br>Member    |   |
| 7      | Dr.S.Jayakumar, M.E., Ph.D., (Applied Electronics)<br>Assistant Professor/Dept. of Chemical Engineering,<br>Adhiyamaan College of Engineering, Hosur. | Interdisciplinary<br>Member    |  |
| 8      | Prof.Dr.N.Ramadass, M.E., Ph.D.,(Communication Systems)<br>Associate Professor/Department of ECE,<br>CEG Campus,Anna University, Chennai.             | University<br>Nominee          |  |
| 9      | Prof.Dr. D. Seshachalam(Embedded systems)<br>Prof & Head/Dept. of ECE<br>BMS college of Engineering, Bangalore.                                       | Academic<br>Council<br>Nominee | - AB -  |
| 10     | Prof.Dr.G.Kavitha(Communication systems)<br>Assistant Professor/ Dept. of ECE,<br>Government College of Engineering, Salem.                           | Academic<br>Council<br>Nominee |  |
| 11     | Smt.N.Dhivya,( Electronics)<br>Technical Lead,HCL technologies,<br>Gigeni, Bangalore.   | Industry<br>Representative     |  |
| 12     | Shri..R.Chandrashekar,<br>HR,QAD Department<br>TVS- Hosur   | Alumnus<br>Member              |  |

Date: 27.10.2018

  
Signature of the Chairman-BoS

Minutes of the Seventeenth Meeting

Minutes of the 19<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 21.04.2018 at 10.00 A.M.

MEMBERS PRESENT

1. Dr. S. Sumathi, Chairman
2. Dr. K.A.Dattatreya, Internal Member
3. Dr. T Menakadevi, Internal Member
4. Prof.M.Sivakumar, Internal Member
5. Prof.S.Chidambaram, Internal Member
6. Dr.E.Saravanakumar, Internal Member
7. Dr.T.S.Udhaya Suriya , Interdisciplinary Member, HoD/BME
8. Dr.L.Ganesan , University Nominee
9. Dr.G.Geetha, Academic Council Nominee
10. Dr.K.J.Shanthi, Academic Council Nominee
11. Shri.J.Ravi Kumar , Industry Representative
12. Shri.N.Thandayuthapani , Alumnus Member

- 19.1 To consider the minutes of the Eighteenth Board of Studies meeting in Electronics and Communication Engineering held on 28.10.2017.

**RESOLVED** that the minutes of the Eighteenth Board of Studies meeting in Electronics and Communication Engineering held on 28.10.2017 have been considered.

- 19.2 To consider the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 – 2016.

**RESOLVED** that the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 for the batch of students admitted in 2015 – 2016 be approved (Annexure I)

- 19.3 To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017 be approved (Annexure II)

- 19.4 To consider the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E.(Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** the Syllabi of "Microprocessors and Microcontrollers with Applications" and "Microprocessors and Microcontrollers Laboratory" for 5th Semester B.E.(Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017 be approved.(Annexure III)

- 19.5 To consider the Syllabi of "Communication Engineering", "Embedded System Design" and "Advanced Microprocessor Laboratory" for 5<sup>th</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of "Communication Engineering", "Embedded System Design", and "Advanced Microprocessor Laboratory" for 5<sup>th</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017 be approved.(Annexure IV)

- 19.6 To consider the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017– 2018.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017– 2018 be approved. (Annexure V)

- 19.7 To consider the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of "Digital Electronics" and "Digital Electronics Laboratory" for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering) programme under the Regulation 2015 (CBCS) for the batch of students admitted in 2017 – 2018 be approved .(Annexure VI)

- 19.8 To consider the Syllabi of "Digital Principles and Systems Design" and "Digital Electronics Laboratory" for 3<sup>rd</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of "Digital Principles and Systems Design" and "Digital Electronics Laboratory" for 3<sup>rd</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018 be approved.(Annexure VII)

- 19.9 To consider the curriculum of B.E Electronics and Communication Engineering under the Regulations 2018 to be followed as per choice based credit systems for the batch of the students to be admitted during 2018-2019.

**RESOLVED** that the curriculum of B.E Electronics and Communication Engineering under the Regulations 2018 to be followed as per choice based credit systems for the batch of the students to be admitted during 2018-2019 be approved .(Annexure VIII)

- 19.10 To consider the Syllabi of 3<sup>rd</sup> Semester M.E (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018 be approved .(Annexure IX)

- 19.11 To consider the Curriculum & Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019.

**RESOLVED** that the Curriculum & Syllabi of 1<sup>st</sup> Semester M.E. (Communication Systems) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019 be approved. (Annexure X)

- 19.12 To consider the Syllabi of 3<sup>rd</sup> Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018 be approved. (Annexure XI)

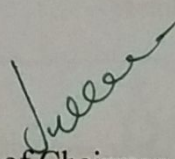
- 19.13 To consider the Curriculum & Syllabi of 1<sup>st</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019.

**RESOLVED** that the Curriculum & Syllabi of 1<sup>st</sup> Semester M.E. (VLSI Design) programme under the Regulations 2018 for the batch of students to be admitted in 2018-2019 be approved. (Annexure XII)

- 19.14 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** that Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved. (Annexure XIII)

Date: 07.04.2018

  
Signature of Chairman – BoS

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Nineteenth Board of Studies Meeting held on 21.04.2018

| Sl.No. | Name & Address of the Members   | Category                       | Signature   |
|--------|---|--------------------------------|-------------|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.  | BoS-Chairman                   |             |
| 2      | Dr. K.A.Dattatreya, M.E (Electronics), MBA, Ph.D<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                |             |
| 3      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.  | Internal Member                |             |
| 4      | Mr.M.Sivakumar, M.E (Communication Systems),<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                |             |
| 5      | Mr.S.Chidambaram.,M.E(Power Electronics & Drives)<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |             |
| 6      | Dr.T.S.Udhaya Suriya ., M.E.,Ph.D.,<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |             |
| 7      | Dr.E.Saravana kumar.,M.E (Computer Science and Engineering),Ph.D<br>Professor , Dept. of CSE, ACE, Hosur.   | Interdisciplinary<br>Member    |             |
| 8      | Prof.Dr.L.Ganesan<br>Prof & Head<br>Department of CSE<br>Alagappa Chettiar College of Engineering and Technology<br>Karaikudi – 630 004.            | University<br>Nominee          | <br>21 4 18 |
| 9      | Prof.Dr.G.Geetha<br>Associate Professor<br>Department of ECE<br>CEG Campus<br>Anna University,Chennai-25  | Academic<br>Council<br>Nominee |             |
| 10     | Prof.Dr.K.J.Shanthi<br>Professor & Head<br>Department of Medical Electronics<br>Dr. Ambedkar Institute of Technology<br>Mallathalli, Bangalore – 56 | Academic<br>Council<br>Nominee |             |
| 11     | Shri.J.Ravi Kumar<br>Proprietor<br>Technilab Instrument<br>Bangalore – 560033   | Industry<br>Representative     |             |
| 12     | Shri.N.Thandayuthapani<br>Senior Software Engineer<br>Robert-Bosch Engineering Business Solutions<br>Bangalore.                                     | Alumnus<br>Member              |             |

Date: 21.04.2018

Signature of the Chairman-BoS  
Chairman, Board of Studies  
Faculty of Electronics and Communication Engg  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.



**ADHIYAMAAN COLLEGE OF ENGINEERING, HOSUR-635 109**  
(Autonomous)  
**BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING**  
**Minutes of the Eighteenth Meeting**

Minutes of the 18<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 28.10.2017 at 10.00 A.M.

**MEMBERS PRESENT**

1. Dr. S. Sumathi, Chairman
2. Dr. K.A.Dattatreya, Internal Member
3. Dr. T Menakadevi, Internal Member
4. Dr.S.Sathish, Internal Member
5. Prof.M.Sivakumar, Internal Member
6. Prof.S.Chidambaram, Internal Member
7. Dr.T.S.Udhaya Suriya , Interdisciplinary Member, HoD/BME
8. Dr.L.Ganesan , University Nominee
9. Dr.G.Geetha, Academic Council Nominee
10. Dr.K.J.Shanthi, Academic Council Nominee
11. Shri.J.Ravi Kumar , Industry Representative
12. Shri.N.Thandayuthapani , Alumnus Member

- 18.1** To consider the minutes of the 17<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 01.04.2017.

**RESOLVED** that the minutes of the 17<sup>th</sup> Board of Studies meeting in Electronics and Communication Engineering held on 01.04.2017 have been considered.

- 18.2** To consider the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2011 for the batch of students admitted in 2014 – 2015.

**RESOLVED** that the Syllabi of 8<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2011 for the batch of students admitted in 2014 – 2015 be approved (Annexure I)

- 18.3** To consider the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2015 – 2016.

**RESOLVED** that the Syllabi of 6<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2015 – 2016 be approved (Annexure II)

- 18.4 To consider the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017 be approved (Annexure III)

- 18.5 To consider the Syllabi of “Microprocessors and Microcontrollers” and “Signals and Systems” for 4<sup>th</sup> Semester B.Tech (Information Technology) programme under Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of “Microprocessors and Microcontrollers” and “Signals and Systems” for 4<sup>th</sup> Semester B.Tech (Information Technology) programme under Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017 be approved (Annexure IV)

- 18.6 To consider the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018.

**RESOLVED** that the Syllabi of “Electric Circuits and Electron Devices” and “Circuits and Devices Laboratory” for 2<sup>nd</sup> Semester B.E (Electronics & Communication Engineering programme) under the Regulations 2015(CBCS) for the batch of students admitted in 2017 – 2018 be approved (Annexure V)

- 18.7 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** that the Syllabi of 2<sup>nd</sup> Semester M.E. (Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2017-2018 be approved (Annexure VI)

- 18.8 To consider the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016-2017.

**RESOLVED** that the Syllabi of 4<sup>th</sup> Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016-2017 be approved (Annexure VII)

18.9 To consider the Syllabi of 2<sup>nd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** that the Syllabi of 2<sup>nd</sup> Semester M.E. (VLSI Design) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018 be approved (Annexure VIII)

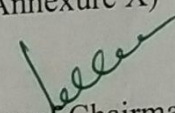
18.10 To consider the Syllabus of "Digital Image Processing" for 2<sup>nd</sup> Semester M.E. (Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018.

**RESOLVED** that the Syllabus of "Digital Image Processing" for 2<sup>nd</sup> Semester M.E. (Computer Science and Engineering) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2017-2018 be approved. (Annexure IX)

18.11 To consider the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering.

**RESOLVED** that the Subjects for course work of Ph.D programme in the Department of Electronics and Communication Engineering be approved (Annexure X)

Date: 28.10.2017

  
Signature of Chairman -BoS

Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

Members Present for the Eighteenth Board of Studies Meeting held on 28.10.2017

| Sl.No. | Name & Address of the Members  | Category                       | Signature    |
|--------|--|--------------------------------|--------------|
| 1      | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.   | BoS-Chairman                   |              |
| 2      | Dr. K.A.Dattatreya, M.E (Electronics), MBA, Ph.D<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |              |
| 3      | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |              |
| 4      | Dr.S.Sathish.,M.E (Communication Systems),MBA,Ph.D<br>Associate Professor,Dept. of ECE, ACE, Hosur.  | Internal Member                |              |
| 5      | Mr.M.Sivakumar,M.E(Communication Systems),<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |              |
| 6      | Mr.S.Chidambaram.,M.E(Power Electronics & Drives)<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                |              |
| 7      | Dr.T.S.Udhaya Suriya<br>Professor & Head<br>Department of BME, ACE, Hosur.   | Interdisciplinary<br>Member    |              |
| 8      | Prof.Dr.L.Ganesan<br>Prof & Head<br>Department of CSE<br>Alagappa Chettiar College of Engineering and<br>Technology,Karaikudi - 630 004.             | University<br>Nominee          | <br>28.10.17 |
| 9      | Prof.Dr.G.Geetha<br>Associate Professor<br>Department of ECE<br>CEG Campus<br>Anna University,Chennai-25.  | Academic<br>Council<br>Nominee |              |
| 10     | Prof.Dr.K.J.Shanthi<br>Professor & Head<br>Department of Medical Electronics<br>Dr. Ambedkar Institute of Technology<br>Mallathalli, Bangalore - 56. | Academic<br>Council<br>Nominee |              |
| 11     | Shri.J.Ravi Kumar<br>Proprietor<br>Technilab Instrument<br>Bangalore - 560033.   | Industry<br>Representative     |              |
| 12     | Shri.N.Thandayuthapani<br>Senior Software Engineer<br>Robert-Bosch Engineering Business Solutions<br>Bangalore.                                      | Alumnus<br>Member              |              |

Signature of the Chairman-BoS

Date: 28.10.2017

**Chairman, Board of Studies**  
**Faculty of Electronics and Communication Engineering (UG & PG)**  
**Adhiyamaan College of Engineering (Autonomous)**  
 Hosur - 635 109  
 Krishnagiri (Dt), Tamil Nadu.

ADHIYAMAAN COLLEGE OF ENGINEERING(AUTONOMOUS), HOSUR-635 109  
BOARD OF STUDIES IN ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the Seventeenth Meeting

Minutes of the 17<sup>th</sup> Meeting of the Board of Studies in Electronics & Communication Engineering held on 01.04.2017 at 10.00 A.M.

MEMBERS PRESENT

1. Dr. S. Sumathi, Chairman
2. Dr. K.A.Dattatreya, Internal Member
3. Dr. T Menakadevi, Internal Member
4. Prof.M.Sivakumar, Internal Member
5. Prof.S.Chidambaram, Internal Member
6. Dr.J.Madhavan, Internal Member
7. Dr.M.C.Jobin Christ , Interdisciplinary Member
8. Dr.L.Ganesan ,University Nominee
9. Dr.G.Geetha, Academic Council Nominee
10. Dr.K.J.Shanthi, Academic Council Nominee
11. Shri.J.Ravi Kumar ,Industry Representative
12. Shri.N.Thandayuthapani , Alumnus Member

- 17.1 To consider the minutes of the Sixteenth Board of Studies meeting in Electronics and Communication Engineering held on 22.10.2016.

**RESOLVED** that the minutes of the Sixteenth Board of Studies meeting in Electronics and Communication Engineering held on 22.10.2016 have been considered.

- 17.2 To consider the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2011 for the batch of students admitted in 2014 – 2015.

**RESOLVED** that the Syllabi of 7<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2011 for the batch of students admitted in 2014 – 2015 be approved (Annexure I)

- 17.3 To consider the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2015 – 2016.

**RESOLVED** that the Syllabi of 5<sup>th</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2015 – 2016 be approved (Annexure II)

- 17.4 To consider the Syllabi of “Microprocessors and Microcontrollers with Applications” and “Microprocessors and Microcontrollers Laboratory” for 5th Semester B.E.(Computer Science and Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2015 – 2016.

**RESOLVED** the Syllabi of “Microprocessors and Microcontrollers with Applications” and “Microprocessors and Microcontrollers Laboratory” for 5th Semester B.E.(Computer Science and Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2015 – 2016 be approved.  
(Annexure III)

- 17.5 To consider the Syllabi of “Communication Engineering”, “Embedded System Design” and “Advanced Microprocessor Laboratory” for 5<sup>th</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2015 – 2016.

**RESOLVED** that the Syllabi of “Communication Engineering”, “Embedded System Design” and “Advanced Microprocessor Laboratory” for 5<sup>th</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2015 – 2016 be approved .(Annexure IV)

- 17.6 To consider the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016– 2017.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester B.E. (Electronics and Communication Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016– 2017 be approved. (Annexure V)

- 17.7 To consider the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of “Digital Electronics” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.E. (Computer Science and Engineering) programme under the Regulation 2015 (CBCS) for the batch of students admitted in 2016 – 2017 be approved.(Annexure VI)

- 17.8 To consider the Syllabi of “Digital Principles and Systems Design” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017.

**RESOLVED** that the Syllabi of “Digital Principles and Systems Design” and “Digital Electronics Laboratory” for 3<sup>rd</sup> Semester B.Tech.(Information Technology) programme under the Regulations 2015(CBCS) for the batch of students admitted in 2016 – 2017 be approved.(Annexure VII)

17.9 To consider the Syllabi of 3<sup>rd</sup> Semester M.E.(Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016-2017.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E.(Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016-2017 be approved .(Annexure VIII)

17.10 To consider the Syllabi of 3<sup>rd</sup> Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016-2017.

**RESOLVED** that the Syllabi of 3<sup>rd</sup> Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students admitted in 2016-2017 be approved (Annexure IX)

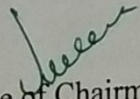
17.11 To consider the Syllabi of 1<sup>st</sup> Semester M.E.(Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students to be admitted in 2017-2018.

**RESOLVED** that the Syllabi of 1<sup>st</sup> Semester M.E.(Communication Systems) programme under the Regulations 2015 (CBCS) for the batch of students to be admitted in 2017-2018 be approved(Annexure X)

17.12 To consider the Syllabi of 1<sup>st</sup> Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students to be admitted in 2017-2018.

**RESOLVED** that the Syllabi of 1<sup>st</sup> Semester M.E.(VLSI Design) programme under the Regulations 2015 (CBCS) for the batch of students to be admitted in 2017-2018 be approved. (Annexure XI)

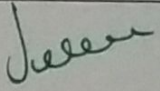
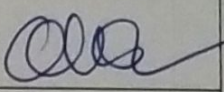
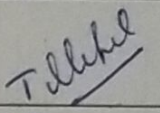

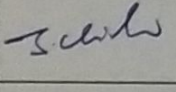
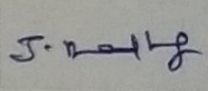
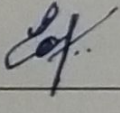
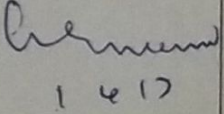
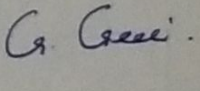
Date: 01.04.2017

  
Signature of Chairman – BoS

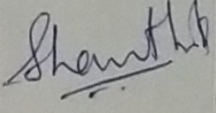
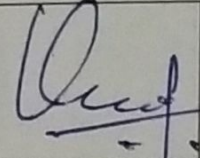
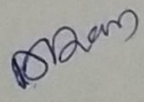
Chairman, Board of Studies  
Faculty of Electronics and Communication Engineering (UG & PG)  
Adhiyamaan College of Engineering (Autonomous)  
Hosur - 635 109  
Krishnagiri (Dt), Tamil Nadu.

**ADHIYAMAAN COLLEGE OF ENGINEERING (AUTONOMOUS), HOSUR-635 109**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

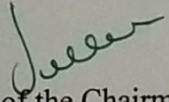
Members Present for the Seventeenth Board of Studies Meeting held on 01.04.2017

| Sl.No | Name & Address of the Members  | Category                       | Signature   |
|-------|--|--------------------------------|---|
| 1     | Dr. S. Sumathi, Ph.D (VLSI),<br>Prof & Head, Dept. of ECE, ACE, Hosur.   | BoS-Chairman                   |            |
| 2     | Dr. K.A.Dattatreya, M.E (Electronics), MBA. Professor,<br>Dept. of ECE, ACE, Hosur.  | Internal Member                |            |
| 3     | Dr. T.Menakadevi.,M.Tech (VLSI),Ph.D.,<br>Professor, Dept. of ECE, ACE, Hosur.   | Internal Member                |            |
| 4     | Mr.M.Sivakumar,M.E(Communication Systems),<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |            |
| 5     | Mr.S.Chidambaram.,M.E(Power Electronics & Drives)<br>AP, Dept. of ECE, ACE, Hosur.   | Internal Member                |            |
| 6     | Dr.J.Madhavan.,M.E (Applied Electronics ),<br>AP, Dept. of ECE, ACE, Hosur.  | Internal Member                |           |
| 7     | Dr.M.C.Jobin Christ (Medical Imaging)<br>Professor & Head<br>Department of BME, ACE, Hosur.  | Interdisciplinary<br>Member    |          |
| 8     | Prof.Dr.L.Ganesan<br>Prof & Head<br>Department of CSE<br>Alagappa Chettiar College of Engineering and<br>Technology Karaikudi - 630 004. | University<br>Nominee          | <br>14/7 |
| 9     | Prof.Dr.G.Geetha<br>Associate Professor<br>Department of ECE<br>CEG Campus<br>Anna University,Chennai-25                                 | Academic<br>Council<br>Nominee |          |



| Sl.No | Name and address of the Member  | Category                    | Signature   |
|-------|---|-----------------------------|---|
| 10    | Prof.Dr.K.J.Shanthi<br>Professor & Head<br>Department of Medical Electronics<br>Dr. Ambedkar Institute of Technology<br>Mallathalli, Bangalore – 56 | Academic Council<br>Nominee |  |
| 11    | Shri.J.Ravi Kumar<br>Proprietor<br>Technilab Instrument<br>Bangalore – 560033   | Industry<br>Representative  |  |
| 12    | Shri.N.Thandayuthapani<br>Senior Software Engineer<br>Robert-Bosch Engineering Business Solutions<br>Bangalore.                                     | Alumnus Member              |  |

Date: 01.04.2017

  
Signature of the Chairman-BoS

**Chairman, Board of Studies**  
**Faculty of Electronics and Communication Engineering (UG & PG)**  
**Adhiyamaan College of Engineering (Autonomous)**  
**Hosur - 635 109**  
**Krishnagiri (Dt), Tamil Nadu.**